## Montreal Preps

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**Reference design** refers to a technical <u>blueprint</u> of a system that is intended for others to copy. It contains the essential elements of the system; however, <u>third parties</u> may enhance or modify the design as required.<sup>[1]</sup> Reference Design Packages enable a fast track to market thereby cutting costs and reducing risk in the customer's integration project. Reference designs enable customers to shorten their time to market. In this modern competitive world shortening the design cycle and speeding up the time to market is the major challenge faced by Original equipment manufacturers (OEMs).

The main purpose of reference design is to support companies in development of next generation products using latest technologies. The reference product is proof of the platform concept and is usually targeted for specific applications.

As the predominant customer for reference designs are OEM's, many reference designs are created by technology component vendors, whether hardware or software, as a means to increase the likelihood that their product will be designed into the OEM's product, giving them a competitive advantage.

Pasted from <<u>http://en.wikipedia.org/wiki/Reference\_design</u>>

Reduced Cobordisms Given by a pair (U,B) of fixed-pt Free involutions (FPEi), and a subset of the Set of cycles of TB Maybe I should just think in forms of cycles and Lotted Cycles L  $V_{-} \rightarrow V_{-} \otimes V_{-}$  $=) \int: V_{\tau} \rightarrow V_{\tau} \otimes V_{-} \neq V_{-} \otimes V_{\tau}$  $\bigvee_{-}\bigvee_{-} \rightarrow 0$  $= \mathbb{I}_{M: V_{-}V_{+} \to V_{-}}$ V+V+-V+

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 $V_{+}V_{+} \rightarrow V_{+}$ 2 7  $\rightarrow Cycks[q[1,2,3,Y], (5,1), (1,7]]$ 5 <u>م</u> 5